

Sheet 1 of 1

FORM PTO-1449  
(REV. 7-80)

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO. 1280.00272

SERIAL NO. 09/707,702

**LIST OF PRIOR ART CITED BY APPLICANT**  
(Use several sheets if necessary)

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JAN 17 2001

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APPLICANT: Dent et al.

FILING DATE: Nov. 7, 2000

GROUP: Unassigned

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

**FOREIGN PATENT DOCUMENTS**

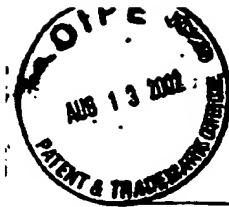
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
S.C.	AL	WO 99 6 3 6 9 6	12/09/99					
	AM							

**OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

S.C.	AN	Kömmeling, Oliver et al. <i>Design Principles for Tamper-Resistant Smartcard Processors</i> , n.d. Advanced Digital Security Research, Mühlstraße 7, 66484 Riedelberg, Germany.
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EXAMINER	<i>LL-Hee Choi</i>	DATE CONSIDERED	6/9/04
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originally filed

**FORM PTO-1449**

**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION PURSUANT TO  
37 CFR §1.97 & 1.98**

Docket Number:	Serial Number:
4015-1598	09/707,702
Applicant:	
Dent et al.	
Filing Date:	Group:
November 7, 2000	2131

## U. S. PATENT DOCUMENTS

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AUG 19 2002

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## FOREIGN PATENT DOCUMENTS

**OTHER DOCUMENTS** (including author, title, date, pages, etc.)

S.C.	B   Messerges, Thomas S., et al., "Power Analysis Attacks of Modular Exponentiation in Smartcards," Cryptographic Hardware and Embedded Systems, International Workshop, Aug. 1999, pp. 144-157
S.C.	C   Coron, J.S., "Resistance Against Differential Power Analysis for Elliptic Curve Cryptosystems," Cryptographic Hardware and Embedded Systems, International Workshop, Aug. 1999, pp. 292-302

**EXAMINER**

### I DATE CONSIDERED

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